



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Anthony Debling
Serial No.: 09/982,094 Patent No. 6,957,179 B2
Filed: October 18, 2001 Issued: October 18, 2005
For: ON-CHIP EMULATOR COMMUNICATION

Examiner: William D. Thomson
Art Unit: 2123 Confirmation No.: 6785

ATTN: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
NOV 04 2005
of Correction

Sir/Madam:

Transmitted herewith for filing is/are the following document(s):

- ☒ Request for Certificate of Correction
- ☒ Copies of: 01/06/05 Prelim Amend and Cols 7 and 8 of U.S. Patent No. 6,957,179.
- ☒ PTO Form SB/44
- ☒ Return Post Card

If the enclosed papers are considered incomplete, the Mail Room and/or the Application Branch is respectfully requested to contact the undersigned collect at (617)720-3500, Boston, Massachusetts.

No check is enclosed. If it is determined that a fee is necessary, the fee may be charged to the account of the undersigned, Deposit Account No. 23/2825. A duplicate of this sheet is enclosed.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this document is being placed in the United States mail with first-class postage attached, addressed to Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the 27th day of October, 2005.

Attorney Docket No.: S1022.80772US00
XNDD

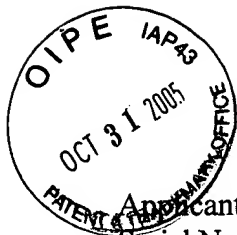
Respectfully submitted,

Anthony Debling, Applicant

By:

James H. Morris, Reg. No.: 34,681
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600 Atlantic Avenue
Boston, Massachusetts 02210
Tel. (617) 720-3500

NOV 08 2005



DOCKET NO.: S1022.80778US00

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**REQUEST FOR CERTIFICATE
OF CORRECTION UNDER 37 C.F.R. §1.323**

Sir/Madam:

Patentee respectfully requests the correction of errors found in the above-captioned patent. Specifically, there are typographical errors in claims 11 and 12 of issued U.S. Patent No. 6,957,179 B2.

Claim 11 as it appeared on page 6 of the Preliminary Amendment filed January 6, 2005 is reproduced below.

11. A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

assigning at least one of said components with a respective address;

sending a remote procedure call from said **component** over a universal serial bus to a computer **device**, said remote procedure call including data indicative of the address of said component;

in response thereto, causing said computer device to generate a socket call over a communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said **universal** serial bus to said component. (Emphasis added)

NOV 08 2005

Claim 11 as it appears in column 7, line 40 through column 8, line 14 is reproduced below.

11. A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

assigning at least one of said components with a respective address;

sending a remote procedure call from said **components** over a universal serial bus to a computer **service**, said remote procedure call including data indicative of the address of said component;

in response thereto, causing said computer device to generate a socket call over a communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said **univesal** serial bus to said component. (Emphasis added)

No amendment was made by the Examiner or Patentee to change the word "component" to "components", the word "device" to "service" or the word "universal" to "univesal" in this claim.

Claim 12 as it appeared on page 6 of the Preliminary Amendment filed January 6, 2005 is reproduced below.

12. A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to **said** digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

assigning plural of said components with a respective address;

sending a remote procedure call from one of said plural components over a universal serial bus to a computer device, said remote procedure call including data indicative of another of said plural components;

in response thereto, causing said computer device to generate a socket call over a communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and
sending information derived from said response over said universal serial
bus to said another component. (Emphasis added)

Claim 12 as it appears in column 8, lines 15-39 is reproduced below.

12. A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to ~~sad~~ digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

assigning plural of said components with a respective address;

sending a remote procedure call from one of said plural components over a universal serial bus to a computer device, said remote procedure call including data indicative of another of said plural components;

in response thereto, causing said computer device to generate a socket call over a communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said universal serial bus to said another component. (Emphasis added)

No amendment was made by the Examiner or Patentee to change the word "said" to "sad" in line 18 of this claim.

In support of the above, Patentee encloses a highlighted copy of the preliminary amendment filed on January 6, 2005 and columns 7 and 8 of U.S. Patent No. 6,957,179. Also enclosed is a Certificate of Correction form PTO Form SB/44.

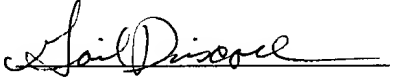
The corrections requested do not involve change in the patent that constitutes new matter or would require reexamination. Therefore, it is respectfully requested that the correction be made and that a Certificate of Correction be issued.

Since neither of the above amendments were made by either Patentee or the Examiner. Patentee respectfully submits that, since the errors for which a Certificate of Correction is sought was the result of Patent Office mistake, no fee is due. However, if the Examiner deems a fee necessary, the fee may be charged to the account of the undersigned, Deposit Account No. 23/2825.

Should any questions arise concerning the foregoing, please contact the undersigned at the telephone number listed below.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

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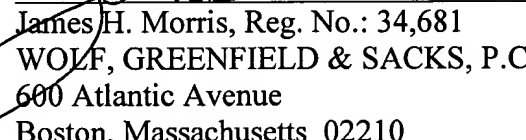
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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,957,179 ⁸²
DATED : October 18, 2005
INVENTOR(S) : Anthony Debling

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 8, claim 11, line 1 should read:

-- sending a remote procedure call from said component--

line 2 should read:

--over a universal serial bus to a computer device, said--

line 14 should read:

--universal serial bus to said component.--

Col. 8, claim 12, line 18 should read:

--lator connected to said digital processing circuitry for initi--

MAILING ADDRESS OF SENDER

PATENT NO. 6,957,179

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NOV 18 2005



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Examiner: William D. Thomson
Art Unit: 2123

Confirmation No.: 6785

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir/Madam:

Prior to examination, please amend this application as follows:

A complete **Listing of the Claims** in this application begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

In the Claims

Please replace all prior versions, and listings, of claims in the application with the following list of claims:

Applicant submits below a complete listing of the current claims, including marked-up claims with insertions indicated by underlining and deletions indicated by strikeouts and/or double bracketing. This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Original) A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

providing universal serial bus having first and second ends, said first end being connected to said on-chip emulator;

providing a computer device having a digital processor, a universal serial bus port connected to said second end of said universal serial bus, and a second port for connection to a communication channel;

assigning at least one of said components with a respective address;

sending a remote procedure call from said component over said universal serial bus to said computer device, said remote procedure call including data indicative of the address of said component;

in response thereto, causing said computer device to generate a socket call over said communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said universal serial bus to said component.

2. (Original) A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

providing a universal serial bus having first and second ends, said first end being connected to said on-chip emulator;

providing a computer device having a digital processor, a universal serial bus port connected to said second end of said universal serial bus, and a second port for connection to a communication channel;

assigning plural of said components with a respective address;

sending a remote procedure call from one of said plural components over said universal serial bus to said computer device, said remote procedure call including data indicative of another of said plural components;

in response thereto, causing said computer device to generate a socket call over said communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said universal serial bus to said another component.

3. (Original) The method of claim 2 further comprising implementing a proxy server process in said computer device, wherein said proxy server process implements said causing and sending steps.

4. (Original) The method of claim 2 wherein said communication channel comprises an Ethernet link.

5. (Currently amended) The method of ~~any of~~ claim 2 wherein said communication channel comprises a telephone link.

6. (Original) A system for communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the system comprising:

a universal serial bus having first and second ends, said first end being connected to said on-chip emulator;

a computer device having a digital processor, a universal serial bus port being connected to said second end of said universal serial bus, and a second port for connection to a communication channel;

generating circuitry associated with said component for sending a remote procedure call from said component over said universal serial bus to said computer device, wherein said remote procedure call comprises data indicative of said component;

conversion circuitry in said computer device for generating a socket call over said communication channel in response to a received remote procedure call thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

receiving circuitry in said computer device for receiving a response at said first socket;
and

sending circuitry for sending information derived from said response over said universal serial bus to said component

7. (Original) A system for communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the system comprising:

a universal serial bus having first and second ends, said first end being connected to said on-chip emulator;

a computer device having a digital processor, a universal serial bus port connected to said second end of said universal serial bus, and a second port for connection to a communication channel;

generating circuitry for sending a remote procedure call from one of said plural components over said universal serial bus to said computer device, including data indicative of another of said plural components;

conversion circuitry in said computer device for generating a socket call over said communication channel in response to a received remote procedure call thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

receiving circuitry in said computer device for receiving a response at said first socket;
and

sending circuitry in said computer device for sending information derived from said response to said another component via said universal serial bus.

8. (Original) The system of claim 7 wherein said computer device comprises an interface device having a universal serial bus port and an Ethernet port for connection to a computer network, whereby said communication channel comprises said computer network.

9. (Original) The system of 7 wherein said communication channel comprises a telephone link.

10. (Currently amended) The system of ~~any of~~ claim 7 wherein said communication channel comprises the Internet.

11. (New) A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

assigning at least one of said components with a respective address;

sending a remote procedure call from said component over a universal serial bus to a computer device, said remote procedure call including data indicative of the address of said component;

in response thereto, causing said computer device to generate a socket call over a communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said universal serial bus to said component.

12. (New) A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

assigning plural of said components with a respective address;

sending a remote procedure call from one of said plural components over a universal serial bus to a computer device, said remote procedure call including data indicative of another of said plural components;

in response thereto, causing said computer device to generate a socket call over a communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said universal serial bus to said another component.

13. (New) The method of claim 12 further comprising implementing a proxy server process in said computer device, wherein said proxy server process implements said causing and sending steps.

14. (New) The method of claim 12 wherein said communication channel comprises an Ethernet link.

15. (New) The method of claim 12 wherein said communication channel comprises a telephone link.

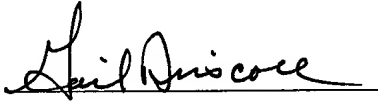
REMARKS

This is a preliminary amendment in which additional claims have been added to further define Applicant's contribution to the art. An early and favorable action is hereby earnestly solicited.

Should any questions arise concerning the application, the Examiner is invited to call the Applicant's attorneys at the number listed below.

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Attorney Docket No.: S1022.80778US00
X01/06/05

Respectfully submitted,

Anthony Debling, Applicant

By: _____

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7

7. A system for communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the system comprising:

a universal serial bus having first and second ends, said first end being connected to said on-chip emulator;

a computer device having a digital processor, a universal serial bus port connected to said second end of said universal serial bus, and a second port for connection to a communication channel;

generating circuitry for sending a remote procedure call from one of said plural components over said universal serial bus to said computer device, including data indicative of another of said plural components;

conversion circuitry in said computer device for generating a socket call over said communication channel in response to a received remote procedure call thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

receiving circuitry in said computer device for receiving a response at said first socket; and

sending circuitry in said computer device for sending information derived from said response to said another component via said universal serial bus.

8. The system of claim 7 wherein said computer device comprises an interface device having a universal serial bus port and an Ethernet port for connection to a computer network, whereby said communication channel comprises said computer network.

9. The system of 7 wherein said communication channel comprises a telephone link.

10. The system of claim 7 wherein said communication channel comprises the Internet.

11. A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

assigning at least one of said components with a respective address;

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sending a remote procedure call from said components over a universal serial bus to a computer service, said remote procedure call including data indicative of the address of said component;

in response thereto, causing said computer device to generate a socket call over a communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said universal serial bus to said component.

12. A method of communicating with an integrated circuit chip having plural components thereon, said components including digital processing circuitry and an on-chip emulator connected to said digital processing circuitry for initiating command and control sequences for the digital processing circuitry in response to externally applied signals or in response to detected states of the digital processing circuitry, the method comprising:

assigning plural of said components with a respective address;

sending a remote procedure call from one of said plural components over a universal serial bus to a computer device, said remote procedure call including data indicative of another of said plural components;

in response thereto, causing said computer device to generate a socket call over a communication channel thereby creating a first socket at said computer device and a second socket at a computer connected to said communication channel;

in said computer device, receiving a response at said first socket; and

sending information derived from said response over said universal serial bus to said another component.

13. The method of claim 12 further comprising implementing a proxy server process in said computer device, wherein said proxy server process implements said causing and sending steps.

14. The method of claim 12 wherein said communication channel comprises an Ethernet link.

15. The method of claim 12 wherein said communication channel comprises a telephone link.

* * * * *